PARALLEL INTERFACE

INPUT SIGNAL SYSTEM DIAGRAM

DATA 1

DATA 2

DATA 3

DATA 4   These DATA lines provide 8 input signals for actuating
                the Printer. The Printer will ignore any invalid code applied.

DATA 5

DATA 6

DATA 7

DATA 8
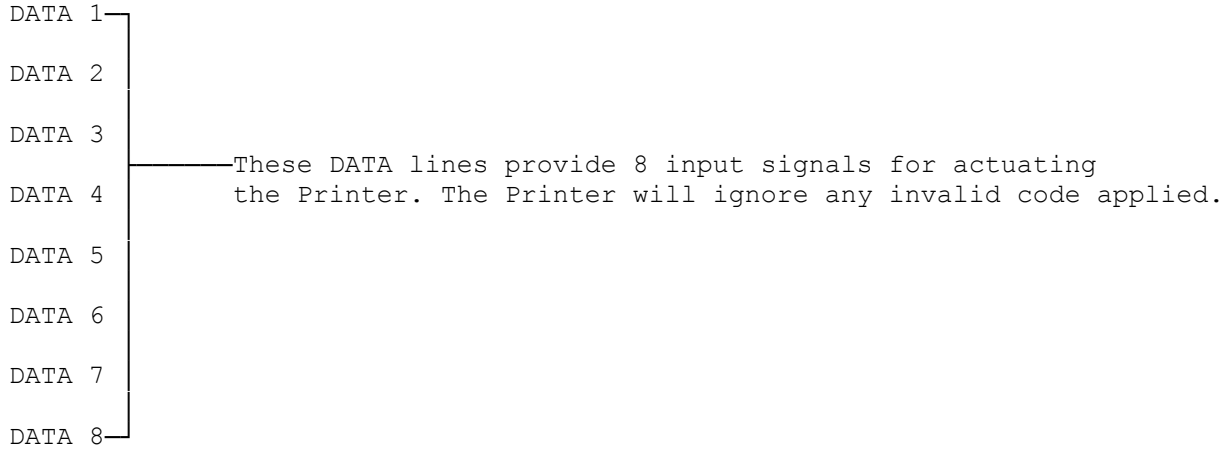
$\overline{STROBE}$ A sampling signal for the DATA lines which provide instruct-
                ion signals for actuating the Printer.

$\overline{INITIALIZE}$ This signal into this terminal initializes the Printer to the
                power ON state. The minimum pulse width

* Ground not shown

OUTPUT SIGNAL SYSTEM DIAGRAM

BUSY   Busy condition: 1) Data is on the buffer 5) Ribbon fault state
                                2) Initial state   6) Cover open state
                                3) Off-line mode   7) Buffer full
                                4) Error state   8) Paper out state

                Ready condition: Condition other than those listed in 1-8
                above.

OUT OF PAPER   Indicates that the Printer is out of paper. This signal is
                effective when the Bidirectional Tractor (26-2813) is mounted on
                the Printer.

$\overline{BUSY}$ The logical inverse of BUSY.

$\overline{ACK}$ Indicates that the Printer has accepted data from the CPU.

$\overline{FAULT}$ Indicates that the Printer is in an error state, ribbon fault
                state, cover open state, paper out state, or off-line state.

INTERFACE SIGNAL PIN ASSIGNMENTS

        TYPE: 36-pin receptacle
       MODEL: C-173011-2 or equivalent
MANUFACTURER: AMP (Japan) Ltd. or equivalent

PIN	SIGNAL NAME	PIN	SIGNAL NAME
1	STROBE*	19	GND
2	DATA 1	20	GND
3	DATA 2	21	GND
4	DATA 3	22	GND
5	DATA 4	23	GND
6	DATA 5	24	GND
7	DATA 6	25	GND
8	DATA 7	26	GND
9	DATA 8	27	GND
10	ACK*	28	GND
11	BUSY	29	GND
12	OUT OF PAPER	30	GND
13	BUSY*	31	NC
14	GND	32	FAULT*
15	NC	33	INIT*
16	GND	34	NC
17	GND	35	NC
18	+5VDC	36	NC

NOTE: NC pins are actually pulled up to 5 VDC through a 4.7K resistor.

Pin 18 provides 5 VDC to the Tandy computer (less than 80 mA of current).

(dtc-07/27/93)